

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Ramsbey, et al.

Application Serial No.: 09/430,366

Filed: October 28, 1999

Title: Method Of Making A Memory Cell

With Polished Insulator Layer

Group Art Unit: 2813

Examiner: Chen, Jack S. J.

Notice of Allowance: October 5, 2004

ISSUE FEE

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir/Madam:

Enclosed, please find the completed "Issue Fee Transmittal" form and a "Payment by Credit Card" form (PTO-2038) in the amount of \$1,370.00 to cover the issue fee. The drawings filed on October 28, 1999 have been accepted as formal drawings by the Examiner.

An early issuance of the present application is respectfully requested.

Respectfully Submitted; FARJAMI & FARJAMI LLP

Dated: 10/19/04

Michael Farjami, Esq.

Reg. No. 38,135

FARJAMI & FARJAMI LLP

Attorneys for Applicant

Michael Farjami, Esq. FARJAMI & FARJAMI LLP 26522 La Alameda Ave., Suite 360 Mission Viejo, California 92691

Tel: (949) 282-1000 Fax: (949) 282-1002

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed: Mail Stop Issue Fee, Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450

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